

# Revisiting Recency Abstraction for JavaScript

## Towards an Intuitive, Compositional, and Efficient Heap Abstraction

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### Abstract

JavaScript is one of the most widely used programming languages. To understand the behaviors of JavaScript programs and to detect possible errors in them, researchers have developed several static analyzers based on the abstract interpretation framework. However, JavaScript provides various language features that are difficult to analyze statically and precisely such as dynamic addition and removal of object properties, first-class property names, and higher-order functions. To alleviate the problem, JavaScript static analyzers often use *recency abstraction*, which refines address abstraction by distinguishing recent objects from summaries of old objects. We observed that while recency abstraction enables more precise analysis results by allowing *strong updates* on recent objects, it is not monotone in the sense that it does not preserve the precision relationship between the underlying address abstraction techniques: for an address abstraction  $A$  and a more precise abstraction  $B$ , recency abstraction on  $B$  may not be more precise than recency abstraction on  $A$ .

Such an unintuitive semantics of recency abstraction makes its composition with various analysis sensitivity techniques also unintuitive. In this paper, we propose a new *singleton abstraction* technique, which provides a monotone refinement relationship of the underlying address abstraction. We prove the monotonicity of singleton abstraction and our prototype implementation shows promising results.

**CCS Concepts** • **Software and its engineering** → **General programming languages**; • **Theory of computation** → *Program analysis*

**Keywords** Address abstraction, recency abstraction, address partition

### 1. Introduction

JavaScript is one of the most widely used programming languages. It is now the 7th popular language [1] and it becomes the *de facto* language for web programming. In the ever-growing

IoT era, the realm of JavaScript may expand even more [2] and understanding and detecting bugs in JavaScript programs are getting more important.

Recently, researchers have presented several static analyzers for JavaScript programs. SAFE [9], TAJIS [6], and WALA [11] statically analyze JavaScript programs based on the abstract interpretation framework. Because they aim for sound static analysis, their analysis results are often imprecise. Thus, each analyzer develops its own analysis techniques to improve the analysis precision [3, 10–12].

For JavaScript static analysis, analyzing “object properties” precisely serves an important role in improving the analysis precision. First, because property names themselves are first-class values, imprecise analysis of property names lead to imprecise analysis of property accesses. Second, since object properties may be added or removed dynamically, precisely analyzing the existence of object properties is challenging. Imprecisely analyzing that a specific property may not exist in an object may result in reporting a false type error. Third, because JavaScript supports higher-order functions, the values of object properties may be functions, which implies that building control flow graphs precisely requires precise analysis of property accesses.

To analyze object properties more precisely, JavaScript static analyzers often use *recency abstraction* [4]. Note that one of main causes of the analysis imprecision is *weak update*, which updates the value of an object property to a join of its old value and a new value. To analyze such updates more precisely, recency abstraction distinguishes the most recently allocated objects from joined old objects and performs weak updates on joined old objects and *strong updates* that replace old values with new values on the recently allocated objects. Thus, recency abstraction enhances the analysis precision for the most recently allocated objects.

Recency abstraction is yet another address abstraction that divides a given (underlying) partition-based address abstraction into two parts: *old* and *recent* addresses. A partition-based address abstraction divides addresses into partitions and uses their powersets as its abstract domain. One example partition-based address abstraction is the *allocation-site abstraction*, which creates partitions by merging all objects created at the same allocation sites. For each partition, recency abstraction distinguishes a recent address that points to the most recently created objects and an old address that points to old objects, and it allows strong updates only on recent addresses. Consider the following code:

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```

 $l_0$  : function  $f()$  { return {} ; };
 $l_1$  : var  $x = f()$ ;
 $l_2$  : var  $y = f()$ ;
 $l_3$  :  $x.p = 1$ ;
 $l_4$  :  $y.p = 2$ ;
 $l_5$  :  $x.p + y.p$ 

```

```

 $l_0$  : var  $obj = \{\}$ ;
 $l_1$  : if ( ? ) {
 $l_2$  :      $obj.a = 1$ ;
 $l_3$  :      $obj = \{\}$ ;
 $l_4$  : }

```

**Figure 1.** A simple example program

Though it is contrived for the presentation brevity, it shows how recency abstraction for the allocation-site abstraction works succinctly. Since the values of  $x$  and  $y$  are objects created at the same allocation site,  $l_0$ , both  $x$  and  $y$  have the same partition  $l_0$  in the allocation-site abstraction. Thus, at the end of the above code, both  $x.p$  and  $y.p$  have **undefined** (the default value for an absent property), 1, and 2 as their values. On the contrary, recency abstraction splits  $l_0$  into two parts:  $(l_0, o)$  for joined old addresses and  $(l_0, r)$  for a recent address. At the end of the above code,  $x$  has the old abstract address  $(l_0, o)$  and  $y$  has the recent abstract address  $(l_0, r)$ . Thus,  $x.p$  has both **undefined** and 1 as its values because of weak updates on the old address, but  $y.p$  has only 2 as its value because of the strong update on the recent address.

While recency abstraction provides more precise analysis than its underlying address abstraction, it is *not monotone* in the sense that it does not preserve the refinement relationship between its underlying address abstraction techniques. We say that a partition-based address abstraction  $A_1$  with a partition  $\delta_1$  is a refinement of another partition-based address abstraction  $A_2$  with a partition  $\delta_2$ , if the partition  $\delta_1$  is finer than the partition  $\delta_2$ . We prove that the refinement relationship is proportional to the analysis precision. Unfortunately, recency abstraction on  $A_1$ , which is a refinement of  $A_2$ , may not be a refinement of recency abstraction on  $A_2$ . Thus, it is unclear which address abstraction would provide the most precise analysis in conjunction with recency abstraction, which denotes that recency abstraction is *not compositional* with other analysis techniques.

In this paper, we present a *singleton abstraction*, which improves the analysis precision of its underlying address abstraction without the aforementioned problems of recency abstraction. The contributions of this paper include the following:

- We formally define recency abstraction on a partition-based address abstraction such as the allocation-site abstraction, and describes how it interferes with address partitioning and analysis sensitivities.
- We propose a singleton abstraction, which enhances the analysis precision while preserving the refinement relationship of its underlying address abstraction. Therefore, it is compositional with other analysis techniques.
- Our preliminary experimental results show that the singleton abstraction provides similar analysis precision as recency abstraction.

In the remaining of this paper, we present the concrete semantics of a simplified variant of JavaScript (Section 2), formally define recency abstraction, and show two code examples illustrating unintuitive behaviors of recency abstraction (Section 3). Then, we propose a new singleton abstraction, which improves the analysis precision without changing its underlying address abstraction (Section 4). After evaluating the analysis precision of the singleton abstraction compared to recency abstraction (Section 5), we discuss related work (Section 6) and conclude (Section 7).

## 2. Concrete Semantics

In this section, we define the concrete semantics of a simplified variant of JavaScript. It contains essential constructs for address abstraction, and we augment the standard concrete semantics with time information to identify recently created objects. We call such time information a *date*, which is a non-negative integer.

### 2.1 Notations and Syntax

We use the following notations:

$l$	$\in$	$\mathbb{L}$	:	control states
$x, y$	$\in$	$\mathbb{X}$	:	variables
$a$	$\in$	$\mathbb{A}$	:	addresses
		$\mathbb{V}_p$	:	primitive values
		$\mathbb{V}$	:	values ( $\mathbb{V} = \mathbb{A} \uplus \mathbb{V}_p$ )
		$\mathbb{D}$	:	dates (non-negative integers)
		$\mathbb{P}$	:	property names of objects (strings)

We let  $\odot$  denote the JavaScript undefined value ( $\odot \in \mathbb{V}_p$ ). A date denotes when an object is created, which is used for recency abstraction. Property names are string values. We consider the following abstract syntax as a simplified variant of JavaScript:

```

Program ::= Func* Stmt*
Func    ::= function Id ( [Id [, Id]*]? ) { Stmt* }
Stmt    ::= var Id [= Expr]?;
        | Id = Expr; | Expr.Prop = Expr;
        | if ( Expr ) { Stmt* } else { Stmt* }
        | return Expr;
Expr    ::= Expr ( Expr* ) | Expr [ Expr ] | Expr.Prop | { }
        | Id | ? | 0 | 1 | + | - |  $\dots$  (values or operators)
Id      ::= x | y |  $\dots$  (variable names)
Prop    ::= a | p |  $\dots$  (property names of objects)

```

We use  $?$  to denote unknown values such as dynamically generated values. Figure 1 shows an example code in this syntax. Given a statement  $s$ , we write  $l_0 : s$ ;  $l_1$ , if  $l_0$  is the control state right before the statement and  $l_1$  is the control state right after it.

### 2.2 States and Traces

A state  $\sigma = (\sigma^L, \sigma^C, \sigma^H, \sigma^D) \in \mathbb{S}$  consists of a control state, a context, a heap, and a date:

$\mathbb{S}$	$=$	$\mathbb{L} \times \mathbb{C} \times \mathbb{H} \times \mathbb{D}$	:	states
$\mathbb{C}$	$=$	$\mathbb{E} \times \mathbb{K}$	:	contexts
$\mathbb{E}$	$=$	$\mathbb{X} \mapsto \mathbb{V}$	:	environments
$\mathbb{K}$	$=$	$\{\epsilon\} \uplus (\mathbb{L} \times \mathbb{C})$	:	call contexts
$\mathbb{H}$	$=$	$\mathbb{A} \mapsto (\mathbb{O} \times \mathbb{L} \times \mathbb{D})$	:	heaps
$\mathbb{O}$	$=$	$\mathbb{P} \mapsto \mathbb{V}$	:	objects

A context consists of an environment and a call context. An environment is a partial map from variables to values. A call context in top-level is  $\epsilon$ ; in a function  $f$ , a call context is a pair of the control point and the context of the call-site of  $f$ . A heap is

a partial map from addresses to objects with their allocation sites and dates. An object is a partial map from property names to their values.

A trace  $\tau \in \mathbb{T}$  is a finite sequence of states  $\langle \sigma_0, \dots, \sigma_{n-1} \rangle$ . The date of a state captures the number of program execution steps so far: in a well-formed trace  $\langle \sigma_0, \dots, \sigma_{n-1} \rangle$ , the date of  $\sigma_i$  is  $i$  and the transition rules in the concrete semantics defined in Section 2.3 ensure this. The following table represents sample traces for the example code in Figure 1. A trace executing the true branch is as follows:

$\sigma_i^L$	$\sigma_i^E(\text{obj})$	$\sigma_i^H$	$\sigma_i^D$
$\ell_0$	$\odot$	$\emptyset$	0
$\ell_1$	$a_{t_0}$	$a_{t_0} \mapsto (\{\}, \ell_0, 0)$	1
$\ell_2$	$a_{t_0}$	$a_{t_0} \mapsto (\{\}, \ell_0, 0)$	2
$\ell_3$	$a_{t_0}$	$a_{t_0} \mapsto (\{\mathbf{a} : 1\}, \ell_0, 0)$	3
$\ell_4$	$a_{t_1}$	$a_{t_0} \mapsto (\{\mathbf{a} : 1\}, \ell_0, 0)$ $a_{t_1} \mapsto (\{\}, \ell_3, 3)$	4

and another trace executing the false branch is as follows:

$\sigma_i^L$	$\sigma_i^E(\text{obj})$	$\sigma_i^H$	$\sigma_i^D$
$\ell_0$	$\odot$	$\emptyset$	0
$\ell_1$	$a_{f_0}$	$a_{f_0} \mapsto (\{\}, \ell_0, 0)$	1
$\ell_5$	$a_{f_0}$	$a_{f_0} \mapsto (\{\}, \ell_0, 0)$	2

### 2.3 Concrete Semantics

We define a small-step semantics characterized by a transition relation  $\rightarrow$ , and use the finite trace semantics induced by  $\rightarrow$ . The initial state is  $(\ell_0, (\emptyset, \epsilon), \emptyset, 0)$  where  $\ell_0$  is the start control point of a given program. The helper function  $\text{eval}(e, \sigma^E)$  evaluates an expression  $e$  with an environment  $\sigma^E$ . For instance, the transitions for simple variable creation and object allocation statements have the following semantics:

- Simple variable creation without initialization

$$\begin{aligned} \ell_0 &: \text{var } \mathbf{x}; \\ \ell_1 &: \dots \end{aligned}$$

$$(\ell_0, (\sigma_0^E, \sigma_0^K), \sigma_0^H, \sigma_0^D) \rightarrow (\ell_1, (\sigma_1^E, \sigma_0^K), \sigma_0^H, \sigma_1^D) \text{ where } \sigma_1^E = \sigma_0^E[\mathbf{x} \mapsto \odot] \text{ and } \sigma_1^D = \sigma_0^D + 1$$

- Object allocation

$$\begin{aligned} \ell_0 &: \mathbf{x} = \{\}; \\ \ell_1 &: \dots \end{aligned}$$

$$(\ell_0, (\sigma_0^E, \sigma_0^K), \sigma_0^H, \sigma_0^D) \rightarrow (\ell_1, (\sigma_1^E, \sigma_0^K), \sigma_1^H, \sigma_1^D) \text{ where } a \text{ is a fresh address, } \sigma_1^E = \sigma_0^E[\mathbf{x} \mapsto a], \sigma_1^H = \sigma_0^H[a \mapsto (\{\}, \ell_0, \sigma_0^D)], \text{ and } \sigma_1^D = \sigma_0^D + 1.$$

The remaining rules are available in Appendix A. Generally, the transition relation  $\rightarrow$  should ensure that, for each transition  $(\sigma_0^L, \sigma_0^C, \sigma_0^H, \sigma_0^D) \rightarrow (\sigma_1^L, \sigma_1^C, \sigma_1^H, \sigma_1^D)$ ,  $\sigma_1^D = \sigma_0^D + 1$ .

## 3. Recency Abstraction

We formally define a series of abstractions towards recency abstraction on top of a given partition-based address abstraction. Then, we illustrate unintuitive behaviors of recency abstraction using two code examples.

### 3.1 Abstractions

We define a program abstraction by composing a series of abstractions:

- a classical flow-sensitive abstraction that maps each control state to the set of states that are observed at that location; and
- an abstraction of sets of states by collapsing addresses according to an address abstraction given as a parameter of the program abstraction.

**Address abstraction** An address abstraction is defined by:

- a set of *abstract addresses*  $\mathbb{A}^\#$  (we note  $a^\#$  for an element of this set); and
- a function  $\phi^A : \mathbb{A} \rightarrow \mathbb{A}^\#$  that maps each address into the abstract address that represents it.

In the following, we consider several choices of this address abstraction. In each case, it fixes fully for each state the mapping between concrete addresses and abstract addresses.

**State abstraction based on address abstraction** Given a pair  $(\mathbb{A}^\#, \phi^A)$ , we can define abstract domains and abstraction functions for the state abstraction as shown in Figure 2. Abstractions of control states and primitive values are their powersets. Because a power set of primitive values could be an infinite set, we should define its finite abstraction in real analysis, but we use powersets in this paper for the presentation brevity. Abstractions of states are a pair of abstractions of environments and heaps. An abstract environment is a map from variables to pairs of abstract addresses and sets of primitive values. An abstract heap is a map from abstract addresses to abstract objects. In the heap abstraction, we merge all the abstract objects corresponding to a given abstract address. Finally, an abstract object is a map from property names to pairs of abstract addresses and sets of primitive values or the special consider  $\otimes$ ; when an abstract object  $o^\#$  has a mapping from  $p$  to  $\otimes$ , it denotes that  $p$  may not exist in  $o^\#$ . Since abstract domains are complete lattices, we define at each step an element-wise abstraction function  $\phi$ , that maps each element to its best abstraction. Such functions implicitly define Galois connections. For instance,  $\mathbb{A}, \phi^A$  defines  $\mathcal{P}(\mathbb{A}) \xrightarrow[\gamma]{\alpha} \mathbb{A}^\#$  by:

$$\alpha(\mathbb{A}') = \bigsqcup_{a \in \mathbb{A}'} \phi^A(a), \quad \gamma(a^\#) = \{a \in \mathbb{A} \mid \phi^A(a) \sqsubseteq a^\#\}$$

**Recency abstraction** Recency abstraction is a commonly used address abstraction for JavaScript analysis, which is often defined on top of the allocation-site abstraction. We generalize the allocation-site abstraction as a partition-based address abstraction, and define recency abstraction on it. Thus, our formalization can represent recency abstraction on heap cloning [8], which is yet another partition-based address abstraction.

A partition-based address abstraction  $(\mathbb{A}_\delta^\#, \phi_\delta^A)$  is defined with a partition  $\delta : \mathbb{A} \rightarrow \Pi$  where  $\mathbb{A}_\delta^\# = \mathcal{P}(\Pi)$  and  $\phi_\delta^A(a) = \{\delta(a)\}$ . We could simplify the heap abstraction using the partition  $\delta$  as follows:

- $\mathbb{H}_\delta^\# = \Pi \rightarrow \mathbb{O}^\#$
- $\phi_\delta^H(h) = \lambda(\pi \in \Pi) \cdot \bigsqcup \{\phi^\mathbb{O}(o) \mid \exists a \in \mathbb{A}, \delta(a) = \pi \wedge h(a) = (o, -, -)\}$

Given a partition-based address abstraction with a partition  $\delta : \mathbb{A} \rightarrow \Pi$ , and a corresponding state  $(\ell, c, h, n)$ , we define recency abstraction as follows:

- $\mathbb{A}_{r[\delta]}^\# = \mathcal{P}(\Pi \times \{\mathbf{r}, \mathbf{o}\})$ ;
- $\phi_{r[\delta]}^A(a) = \begin{cases} \{(\pi, \mathbf{r})\} & \text{if } h(a) = (o, \ell, n_r) \\ \{(\pi, \mathbf{o})\} & \text{otherwise} \end{cases}$

Concrete domain	Abstract domain	Element-wise abstraction function
$\mathbb{S}$	$\mathbb{S}^\# = \mathbb{E}^\# \times \mathbb{H}^\#$	$\phi^{\mathbb{S}}((-, (e, -), h, -)) = (\phi^{\mathbb{E}}(e), \phi^{\mathbb{H}}(h))$
$\mathbb{E}$	$\mathbb{E}^\# = \mathbb{X} \longrightarrow \mathbb{A}^\# \times \mathcal{P}(\mathbb{V}_p)$	$\phi^{\mathbb{E}}(e) = \lambda(x \in \text{Domain}(e)) \cdot \begin{cases} (\phi^{\mathbb{A}}(e(x)), \{\}) & \text{if } e(x) \text{ is an address} \\ (\perp, \{e(x)\}) & \text{if } e(x) \text{ is a primitive value} \end{cases}$
$\mathbb{H}$	$\mathbb{H}^\# = \mathbb{A}^\# \longrightarrow \mathbb{O}^\#$	$\phi^{\mathbb{H}}(h) = \lambda(a^\# \in \mathbb{A}^\#) \cdot \bigsqcup \{\phi^{\mathbb{O}}(o) \mid \exists a \in \mathbb{A}, \phi^{\mathbb{A}}(a) = a^\# \wedge h(a) = (o, -, -)\}$
$\mathbb{O}$	$\mathbb{O}^\# = \mathbb{P} \longrightarrow \mathbb{A}^\# \times \mathcal{P}(\mathbb{V}_p \uplus \{\otimes\})$	$\phi^{\mathbb{O}}(o) = \lambda(p) \cdot \begin{cases} (\phi^{\mathbb{A}}(o(p)), \{\}) & \text{if } o(p) \text{ is an address} \\ (\perp, \{o(p)\}) & \text{if } o(p) \text{ is a primitive value} \\ (\perp, \{\otimes\}) & \text{if } p \notin \text{Domain}(o) \end{cases}$

**Figure 2.** Abstractions based on the address abstraction  $(\mathbb{A}^\#, \phi^{\mathbb{A}})$

where  $\pi = \delta(a)$

and  $n_r = \max\{n' \mid \exists a' \in \mathbb{A}, o' \in \mathbb{O}, l' \in \mathbb{L}, \delta(a') = \pi \wedge h(a') = (o', l', n')\}$ .

This allows to abstract sets of states similarly as above except that the address abstraction function depends on states.

### 3.2 Unintuitive Behaviors of Recency Abstraction

Now, we illustrate unintuitive behaviors of recency abstraction using two examples.

**Example 1** The code in Figure 1 contains two allocation-sites  $l_0$  and  $l_3$ . Let us consider two partition-based address abstractions: the allocation-site abstraction with  $\delta_{id} : \mathbb{A} \rightarrow \mathbb{L}$ , which divides addresses based on their allocation sites and a crude one with  $\delta_\top : \mathbb{A} \rightarrow \{\pi\}$  for some  $\pi$ , which does not partition at all. Clearly, the abstraction  $(\mathbb{A}_{\delta_{id}}^\#, \phi_{\delta_{id}}^{\mathbb{A}})$  defines a more precise address partition than  $(\mathbb{A}_{\delta_\top}^\#, \phi_{\delta_\top}^{\mathbb{A}})$ . Unfortunately, recency abstraction does not preserve this “more precise than” relationship. Let’s look at the analysis results at the control state  $l_4$ . The abstraction with recency abstraction  $(\mathbb{A}_{r[\delta_{id}]}^\#, \phi_{r[\delta_{id}]}^{\mathbb{A}})$  produces the following result:

	$e^\#$	$h^\#$
true branch	$\text{obj} \mapsto \{(l_3, \mathbf{r})\}$	$(l_0, \mathbf{r}) \mapsto \{\mathbf{a} \mapsto \{1\}\}$ $(l_3, \mathbf{r}) \mapsto \{\}$
false branch	$\text{obj} \mapsto \{(l_0, \mathbf{r})\}$	$(l_0, \mathbf{r}) \mapsto \{\}$
join	$\text{obj} \mapsto \{(l_0, \mathbf{r}), (l_3, \mathbf{r})\}$	$(l_0, \mathbf{r}) \mapsto \{\mathbf{a} \mapsto \{\otimes, 1\}\}$ $(l_3, \mathbf{r}) \mapsto \{\}$

The joined result of both true and false branches shows that  $\text{obj.a}$  may have values  $\{\otimes, 1\}$ . On the contrary, the abstraction with  $(\mathbb{A}_{r[\delta_\top]}^\#, \phi_{r[\delta_\top]}^{\mathbb{A}})$  produces the following:

	$e^\#$	$h^\#$
true branch	$\text{obj} \mapsto \{(\pi, \mathbf{r})\}$	$(\pi, \mathbf{r}) \mapsto \{\}$ $(\pi, \mathbf{o}) \mapsto \{\mathbf{a} \mapsto \{1\}\}$
false branch	$\text{obj} \mapsto \{(\pi, \mathbf{r})\}$	$(\pi, \mathbf{r}) \mapsto \{\}$
join	$\text{obj} \mapsto \{(\pi, \mathbf{r})\}$	$(\pi, \mathbf{r}) \mapsto \{\}$ $(\pi, \mathbf{o}) \mapsto \{\mathbf{a} \mapsto \{1\}\}$

The joined result of both branches shows that  $\mathbf{a}$  does not exist in  $\text{obj}$ ; thus, the value of  $\text{obj.a}$  is  $\{\otimes\}$ . This example shows that  $(\mathbb{A}_{r[\delta_\top]}^\#, \phi_{r[\delta_\top]}^{\mathbb{A}})$  is more precise than  $(\mathbb{A}_{r[\delta_{id}]}^\#, \phi_{r[\delta_{id}]}^{\mathbb{A}})$  while  $(\mathbb{A}_{\delta_{id}}^\#, \phi_{\delta_{id}}^{\mathbb{A}})$  is more precise than  $(\mathbb{A}_{\delta_\top}^\#, \phi_{\delta_\top}^{\mathbb{A}})$ . Therefore, the precision relationship of the underlying address abstraction is not preserved with recency abstraction.

**Example 2** The code in Figure 3 shows that recency abstraction may interfere with analysis sensitivities. Let us consider two

```

l0 : function g(z){
l1 :   var result = z.p;
l2 :   }
l3 : function f(){
l4 :   var obj = {};
l5 :   var a = g(obj);
l6 :   obj.p = 3;
l7 :   return obj;
l8 : }
l9 : var x = f();
l10 : var y = f();
l11 :

```

**Figure 3.** Recency abstraction interfering with sensitivities

analysis sensitivities: 1-CFA that distinguishes the same function from its different call sites using its caller, and 0-CFA that does not distinguish different call sites of the same function. Then, we consider the allocation-site abstraction refined by different sensitivities. With 1-CFA, the partition is  $\delta : \mathbb{A} \rightarrow \{l_{4/9}, l_{4/10}\}$  where  $l_{4/9}$  means that the allocation-site  $l_4$  with the call-site  $l_9$  and  $l_{4/10}$  means that the allocation-site  $l_4$  with the call-site  $l_{10}$ . In this case, we get the following result at the control state  $l_1$ :

	$e^\#$	$h^\#$
call $l_9, l_5$	$\mathbf{z} \mapsto \{(l_{4/9}, \mathbf{r})\}$	$(l_{4/9}, \mathbf{r}) \mapsto \{\}$
call $l_{10}, l_5$	$\mathbf{z} \mapsto \{(l_{4/10}, \mathbf{r})\}$	$(l_{4/9}, \mathbf{r}) \mapsto \{\mathbf{p} \mapsto \{3\}\}$ $(l_{4/10}, \mathbf{r}) \mapsto \{\}$
join	$\mathbf{z} \mapsto \{(l_{4/9}, \mathbf{r}), (l_{4/10}, \mathbf{r})\}$	$(l_{4/9}, \mathbf{r}) \mapsto \{\mathbf{p} \mapsto \{\otimes, 3\}\}$ $(l_{4/10}, \mathbf{r}) \mapsto \{\}$

With 0-CFA, the partition is  $\delta : \mathbb{A} \rightarrow \{l_4\}$ . Thus, it has only one partition and we get the following result at the control state  $l_1$ :

	$e^\#$	$h^\#$
call $l_9, l_5$	$\mathbf{z} \mapsto \{(l_4, \mathbf{r})\}$	$(l_4, \mathbf{r}) \mapsto \{\}$
call $l_{10}, l_5$	$\mathbf{z} \mapsto \{(l_4, \mathbf{r})\}$	$(l_4, \mathbf{r}) \mapsto \{\}$ $(l_4, \mathbf{o}) \mapsto \{\mathbf{p} \mapsto \{3\}\}$
join	$\mathbf{z} \mapsto \{(l_4, \mathbf{r})\}$	$(l_4, \mathbf{r}) \mapsto \{\}$ $(l_4, \mathbf{o}) \mapsto \{\mathbf{p} \mapsto \{3\}\}$

This example shows that a more precise 1-CFA may produce less precise results than 0-CFA when combined with recency abstraction. Therefore, the precision relationship of analysis sensitivities is not preserved when combined with recency abstraction.

## 4. Singleton Abstraction

In this section, we explain the unintuitive behaviors of recency abstraction in terms of the refinement relationship between

partition-based address abstractions. Then, we present *singleton abstraction*, a new heap abstraction based on a given partition-based address abstraction, which preserves the refinement relationship of its underlying address abstraction and moreover allows strong updates on singleton addresses.

#### 4.1 Refinement of Address Abstraction

We first define terminologies to discuss the behaviors of recency abstraction. A partition-based address abstractions  $(\mathbb{A}_{\delta_i}^{\#}, \phi_{\delta_i}^{\Delta})$  is defined with a partition  $\delta_i : \mathbb{A} \rightarrow \Pi_i$ . A partition-based address abstraction is a refinement of another, if and only if their partitions have the refinement relationship accordingly.

**Definition 1** ( $\preceq$ ).  $(\mathbb{A}_{\delta_1}^{\#}, \phi_{\delta_1}^{\Delta}) \preceq (\mathbb{A}_{\delta_2}^{\#}, \phi_{\delta_2}^{\Delta})$  iff  $\delta_1$  is a refinement partition of  $\delta_2$ .

An address abstraction  $(\mathbb{A}_1^{\#}, \phi_1^{\Delta})$  is more precise than  $(\mathbb{A}_2^{\#}, \phi_2^{\Delta})$  if and only if the concretization of the former is a subset of that of the latter.

**Definition 2** ( $\preceq_p$ ).  $(\mathbb{A}_1^{\#}, \phi_1^{\Delta}) \preceq_p (\mathbb{A}_2^{\#}, \phi_2^{\Delta})$  iff  $\gamma_1 \circ \alpha_1 \subseteq \gamma_2 \circ \alpha_2$ .

Then, we prove that the refinement relation implies the precision relation.

**Theorem 1** (Implication of precision from refinement).

$$(\mathbb{A}_{\delta_1}^{\#}, \phi_{\delta_1}^{\Delta}) \preceq (\mathbb{A}_{\delta_2}^{\#}, \phi_{\delta_2}^{\Delta}) \Rightarrow (\mathbb{A}_{\delta_1}^{\#}, \phi_{\delta_1}^{\Delta}) \preceq_p (\mathbb{A}_{\delta_2}^{\#}, \phi_{\delta_2}^{\Delta})$$

**Proof.** Let us consider the Galois connections  $\mathcal{P}(\mathbb{A}) \xrightarrow[\gamma_1]{\alpha_1} \mathbb{A}_{\delta_1}^{\#}$  and  $\mathcal{P}(\mathbb{A}) \xrightarrow[\gamma_2]{\alpha_2} \mathbb{A}_{\delta_2}^{\#}$  defined as above. Given  $\mathbb{A}' \subseteq \mathbb{A}$ , there exists  $\Pi'_1 \subseteq \Pi_1$  s.t.  $\gamma_1(\Pi'_1) = \gamma_2(\Pi'_2)$  where  $\Pi'_2 = \alpha_2(\mathbb{A}')$  because  $(\mathbb{A}_{\delta_1}^{\#}, \phi_{\delta_1}^{\Delta}) \preceq (\mathbb{A}_{\delta_2}^{\#}, \phi_{\delta_2}^{\Delta})$ . It means that  $\alpha_1(\mathbb{A}') \sqsubseteq \Pi'_1$ . Therefore,  $\gamma_1 \circ \alpha_1(\mathbb{A}') \subseteq \gamma_1(\Pi'_1) = \gamma_2(\Pi'_2) = \gamma_2 \circ \alpha_2(\mathbb{A}')$ .  $\square$

Now, let us revisit the first example in Section 3.2 with the refinement relation. Because  $\delta_{id}$  is a partition of  $\delta_{\top}$ , we have  $(\mathbb{A}_{\delta_{id}}^{\#}, \phi_{\delta_{id}}^{\Delta}) \preceq (\mathbb{A}_{\delta_{\top}}^{\#}, \phi_{\delta_{\top}}^{\Delta})$ . The recency abstraction with a cruder partition  $(\mathbb{A}_{r[\delta_{\top}]}^{\#}, \phi_{r[\delta_{\top}]}^{\Delta})$  has two partitions  $(\pi, \mathbf{r})$  and  $(\pi, \mathbf{o})$ :  $\gamma((\pi, \mathbf{r})) = \{a_{t_1}, a_{f_0}\}$  and  $\gamma((\pi, \mathbf{o})) = \{a_{t_0}\}$  where  $a_{t_0}$  and  $a_{t_1}$  are concrete addresses created at  $l_0$  and  $l_3$ , respectively, for the true branch, and  $a_{f_0}$  is a concrete address created at  $l_0$  for the false branch. The other recency abstraction  $(\mathbb{A}_{r[\delta_{id}]}^{\#}, \phi_{r[\delta_{id}]}^{\Delta})$  has four partitions, and only two partitions  $(l_0, \mathbf{r})$  and  $(l_3, \mathbf{r})$  have elements:  $\gamma((l_0, \mathbf{r})) = \{a_{t_0}, a_{f_0}\}$  and  $\gamma((l_3, \mathbf{r})) = \{a_{t_1}\}$ . Thus,  $(\mathbb{A}_{r[\delta_{id}]}^{\#}, \phi_{r[\delta_{id}]}^{\Delta}) \not\preceq (\mathbb{A}_{r[\delta_{\top}]}^{\#}, \phi_{r[\delta_{\top}]}^{\Delta})$ , which illustrates a case where recency abstraction does not preserve the refinement relation of its underlying address abstraction, which in turn does not preserve their precision relation. Similarly, the second example shows that recency abstraction with a more precise 1-CFA analysis sensitivity does not always produce more precise analysis results than recency abstraction with a less precise 0-CFA.

#### 4.2 Singleton Abstraction

To alleviate the problem, we decide not to divide a given partition but to simply perform strong updates on singleton objects. Thus, we propose singleton abstraction, a new heap abstraction that preserves the refinement relationship of its underlying address abstraction. It can provide more precise analysis results

Bench	Program	LOC	Recency	Singleton	Total
JSAI	adn-chess.js	234	90	55	127
	adn-coffee_pods_deals.js	367	45	37	141
	adn-less_spam_please.js	759	213	143	432
	adn-live_pagerank.js	882	132	117	323
	adn-odesk_job_watcher.js	168	56	52	71
	adn-pinpoints.js	548	58	57	232
	adn-tryagain.js	929	103	72	525
SunSpider	3d-morph.js	23	1	1	4
	access-binary-trees.js	38	14	10	16
	access-fannkuch.js	51	1	1	19
	access-nbody.js	142	32	15	78
	access-nsieve.js	28	2	0	4
	bitops-3bit-bits-in-byte.js	13	0	0	0
	bitops-bits-in-byte.js	14	0	0	0
	bitops-bitwise-and.js	3	0	0	0
	bitops-nsieve-bits.js	22	1	1	7
	controlflow-recursive.js	18	0	0	0
	math-cordic.js	53	4	4	6
	math-partial-sums.js	25	4	4	4
	math-spectral-norm.js	41	2	1	16
string-fasta.js	70	15	10	18	
V8	navier-stokes.js	331	36	17	92
	richards.js	288	119	117	197
	splay.js	205	108	108	132
Total			1036	831	2,444
Ratio (%)			42.39	33.63	—

**Table 1.** Numbers of object property loads that have more precise results with recency or singleton abstraction than the allocation-site abstraction.

with more precise underlying address abstractions and with more precise analysis sensitivities.

Given a partition-based address abstraction  $(\mathbb{A}_{\delta}^{\#}, \phi_{\delta}^{\Delta})$  with a partition  $\delta = \mathbb{A} \rightarrow \Pi$ , we define singleton abstraction as follows:

- $\mathbb{H}_{s[\delta]}^{\#} = \Pi \rightarrow \mathbb{O}^{\#} \times \{\mathbf{s}, \mathbf{m}\}$ ;
- $\phi_{s[\delta]}^{\#}(h) = \lambda(\pi \in \Pi) \cdot (\phi_{\delta}^{\#}(\pi), \begin{cases} \mathbf{s} & \text{if } |U| = 1 \\ \mathbf{m} & \text{otherwise} \end{cases})$   
where  $U = \{a' \in \mathbb{A} \mid \delta(a') = \pi \wedge a' \in \text{Domain}(h)\}$ .

It distinguishes partitions with only one address as  $\mathbf{s}$  and maps the other partitions to  $\mathbf{m}$ . Merging two mappings from the same partition to both singleton ( $\mathbf{s}$ ) and multiple ( $\mathbf{m}$ ) results in  $\mathbf{m}$ .

Unlike recency abstraction, the singleton abstraction preserves the refinement relation of its underlying address abstraction because they use the same partition from the underlying address abstraction. While the expressive power of the singleton abstraction is the same as its partition-based address abstraction, singleton abstraction allows strong updates for address partitions that map to  $\mathbf{s}$ . It permits strong updates on objects created at specific allocation sites.

## 5. Evaluation

We evaluate the precision of singleton abstraction in comparison with recency abstraction. We conducted experiments with 3 sets of benchmarks—JSAI, SunSpider, and V8—consisting of 24 programs on a 2.8 GHz Intel Core i5 iMac with 16GB memory. We implemented 3 address abstractions—allocation-site abstraction, recency abstraction, and singleton abstraction—on an open-

source JavaScript static analysis framework, SAFE [9]. The implemented recency abstraction and singleton abstraction are built on top of the allocation-site abstraction.

The analyses took on average 86.92, 122.73, and 79.77 seconds for the allocation-site, recency, and singleton abstractions, respectively. It means that singleton abstraction does not incur much performance overhead like recency abstraction while providing comparable analysis precision with recency abstraction. We observed that the more complex benchmark programs get, the more performance overhead recency abstraction causes.

For the analysis precision, we compare the numbers of object property loads like `obj.p` that have more precise results with recency or singleton abstraction compared with just the allocation-site abstraction. Table 1 summarizes the experimental results; the 3rd column shows the lines of code, the 4th and the 5th columns show the numbers of more precise property loads by recency and singleton abstractions, respectively, and the last column shows the total number of property loads in each program. For example, the first program in the JSAI benchmarks, `adn-chess.js`, has 127 property loads, among which recency abstraction analyzes 90 property loads more precisely than the allocation-site abstraction. In summary, recency and singleton abstractions analyze about 42.39% and 33.63% of property loads more precisely on average, respectively. Note that recency abstraction divides partitions into two parts: recent and old. Therefore, recency abstraction provides more precise analysis results than singleton abstraction when programs update recent addresses and their allocation sites also have old addresses pointing to different shapes of objects. We plan to extend the set of benchmark programs to understand the relationships between recency and singleton abstractions more clearly.

## 6. Related Work

Among various research directions on heap abstraction, store-based heap models are the abstractions that represent abstract addresses as graph nodes [7]. The allocation-site abstraction is the most commonly used store-based heap model and recency abstraction is also store-based. In addition, our singleton abstraction is also a store-based heap model.

Recency abstraction was first proposed by Balakrishnan *et al.* [4] in 2006 to resolve virtual function calls in C++ by supporting strong updates. While existing approaches allow only weak updates on abstract malloc blocks because they may represent summary blocks containing multiple blocks, recency abstraction permits strong updates for malloc blocks, which could resolve 55% of virtual function calls in their experiments. However, they use only the allocation-site address abstraction as the underlying address abstraction for recency. On the contrary, we generalized the underlying address abstraction to partition-based address abstraction that includes the allocation-site abstraction.

Heidegger *et al.* applied recency abstraction for JavaScript analysis [5]. While they captured recency information in a type system and developed an inference algorithm using a constraint solver for it, we defined recency abstraction entirely based on the abstract interpretation framework.

Most JavaScript static analyzers now use recency abstraction based on the allocation-site abstraction [6, 9], and they provide an option to use heap cloning [8]. The heap cloned allocation-site

abstraction is also a partition-based address abstraction, which is a refinement of the allocation-site abstraction. Because we defined recency abstraction on top of a partition-based address abstraction, our formalization of the recency abstraction covers the recency abstraction implementation in real-world JavaScript analyzers.

## 7. Conclusion

We revisited recency abstraction, a typical address abstraction technique for static analysis of JavaScript programs. We formally defined it on a partition-based address abstraction, and we used the formalization to describe unintuitive behaviors of recency abstraction. We explained the behaviors by showing that recency abstraction does not preserve the refinement relationship between its underlying address abstractions. Thus, it is difficult to predict which address abstraction would provide the most precise analysis result for recency abstraction.

Thus, we proposed singleton abstraction, a new heap abstraction using a partition-based abstraction. It preserves the refinement relationship of the underlying address abstractions. Therefore, it is compositional with other analysis techniques. Moreover, our preliminary experiments showed that it provides similar analysis precision with recency abstraction while reducing the performance overhead.

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## References

- [1] TIOBE Index for February 2017. <http://www.tiobe.com/tiobe-index>.
- [2] Iot.js: A framework for Internet of Things. <http://samsung.github.io/jerryscript/>, 2015.
- [3] E. Andreasen and A. Møller. Determinacy in static analysis for jQuery. In *OOPSLA*, 2014.
- [4] G. Balakrishnan and T. Reps. Recency-abstraction for heap-allocated storage. In *SAS*, 2006.
- [5] P. Heidegger and P. Thiemann. Recency types for analyzing scripting languages. In *ECOOP*, 2010.
- [6] S. H. Jensen, A. Møller, and P. Thiemann. Type analysis for JavaScript. In *SAS*, 2009.
- [7] V. Kanvar and U. P. Khedker. Heap abstractions for static analysis. In *ACM Computing Surveys (CSUR)*, 2016.
- [8] C. Lattner, A. Lenharth, and V. Adve. Making context-sensitive points-to analysis with heap cloning practical for the real world. In *PLDI*, 2007.
- [9] H. Lee, S. Won, J. Jin, J. Cho, and S. Ryu. SAFE: Formal specification and implementation of a scalable analysis framework for ECMAScript. In *FOOL*, 2012.
- [10] C. Park and S. Ryu. Scalable and precise static analysis of JavaScript applications via loop-sensitivity. In *ECOOP*, 2015.
- [11] M. Schäfer, M. Sridharan, J. Dolby, and F. Tip. Dynamic determinacy analysis. In *PLDI*, 2013.
- [12] M. Sridharan, J. Dolby, S. Chandra, M. Schäfer, and F. Tip. Correlation tracking for points-to analysis of JavaScript. In *ECOOP*, 2012.

## A. Semantics of “ $\rightarrow$ ”

We define the semantics of  $\rightarrow$  for the simplified JavaScript. For each statement or expression that changes a state, we provide its state transition rule. In the rules, the special variable RET initialized to the undefined value  $\odot$  denotes return values. The helper function  $\text{eval}(e, \sigma)$  evaluates an expression  $e$  with a state  $\sigma$ . We write  $\sigma_i$  to denote the state at the control state  $l_i$ .

### 1. Simple variable creation without initialization

$$\begin{aligned} l_0 &: \text{var } x; \\ l_1 &: \dots \end{aligned}$$

$$(l_0, (\sigma_0^E, \sigma_0^K), \sigma_0^H, \sigma_0^D) \rightarrow (l_1, (\sigma_1^E, \sigma_0^K), \sigma_0^H, \sigma_1^D) \text{ where}$$

$$\begin{aligned} \sigma_1^E &= \sigma_0^E[x \mapsto \odot] \\ \sigma_1^D &= \sigma_0^D + 1 \end{aligned}$$

### 2. Variable creation with initialization

$$\begin{aligned} l_0 &: \text{var } x = e; \\ l_1 &: \dots \end{aligned}$$

$$(l_0, (\sigma_0^E, \sigma_0^K), \sigma_0^H, \sigma_0^D) \rightarrow (l_1, (\sigma_1^E, \sigma_0^K), \sigma_0^H, \sigma_1^D) \text{ where}$$

$$\begin{aligned} \text{eval}(e, \sigma_0) &= v \\ \sigma_1^E &= \sigma_0^E[x \mapsto v] \\ \sigma_1^D &= \sigma_0^D + 1 \end{aligned}$$

### 3. Function call statement

$$\begin{aligned} l_0 &: \text{function } f(p_0, \dots, p_{n-1}) \{ \\ l_1 &: \dots \\ l_2 &: \} \\ l_3 &: x = f(e_0, \dots, e_{n-1}); \\ l_4 &: \dots \end{aligned}$$

$$(l_3, (\sigma_3^E, \sigma_3^K), \sigma_3^H, \sigma_3^D) \rightarrow (l_4, (\sigma_4^E, \sigma_3^K), \sigma_3^H, \sigma_4^D) \text{ where}$$

$$\begin{aligned} \text{eval}(e_i, \sigma_3) &= v_i \\ \sigma_4^E &= \{\text{RET} \mapsto \odot, p_i \mapsto v_i\} \\ \sigma_4^K &= (\sigma_3^K, (\sigma_3^E, \sigma_3^K)) \\ \sigma_4^D &= \sigma_3^D + 1 \end{aligned}$$

$$(l_2, (\sigma_2^E, \sigma_2^K), \sigma_2^H, \sigma_2^D) \rightarrow (l_4, (\sigma_4^E, \sigma_4^K), \sigma_2^H, \sigma_4^D) \text{ where}$$

$$\sigma_2^K = (\_, \_, (e, k)), \sigma_2^E(\text{RET}) = v, \text{ and:}$$

$$\begin{aligned} \sigma_4^E &= e[x \mapsto v] \\ \sigma_4^K &= k \\ \sigma_4^D &= \sigma_2^D + 1 \end{aligned}$$

### 4. Allocation statement

$$\begin{aligned} l_0 &: x = \{ \}; \\ l_1 &: \dots \end{aligned}$$

$$(l_0, (\sigma_0^E, \sigma_0^K), \sigma_0^H, \sigma_0^D) \rightarrow (l_1, (\sigma_1^E, \sigma_0^K), \sigma_0^H, \sigma_1^D) \text{ where } a \text{ is a}$$

fresh address, and:

$$\begin{aligned} \sigma_1^E &= \sigma_0^E[x \mapsto a] \\ \sigma_1^H &= \sigma_0^H[a \mapsto (\{ \}, l_0, \sigma_0^D)] \\ \sigma_1^D &= \sigma_0^D + 1 \end{aligned}$$

### 5. Assignment statement

$$\begin{aligned} l_0 &: x = e; \\ l_1 &: \dots \end{aligned}$$

$$(l_0, (\sigma_0^E, \sigma_0^K), \sigma_0^H, \sigma_0^D) \rightarrow (l_1, (\sigma_1^E, \sigma_0^K), \sigma_0^H, \sigma_1^D) \text{ where}$$

$$\begin{aligned} \text{eval}(e, \sigma_0) &= v \\ \sigma_1^E &= \sigma_0^E[x \mapsto v] \\ \sigma_1^D &= \sigma_0^D + 1 \end{aligned}$$

### 6. Property store statement

$$\begin{aligned} l_0 &: x.p = e; \\ l_1 &: \dots \end{aligned}$$

$$(l_0, (\sigma_0^E, \sigma_0^K), \sigma_0^H, \sigma_0^D) \rightarrow (l_1, (\sigma_1^E, \sigma_0^K), \sigma_1^H, \sigma_1^D) \text{ where}$$

$$\sigma_0^E(x) = a, \sigma_0^H(a) = o, \text{eval}(e, \sigma_0) = v, \text{ and:}$$

$$\begin{aligned} \sigma_1^H &= \sigma_0^H[a \mapsto o[p \mapsto v]] \\ \sigma_1^D &= \sigma_0^D + 1 \end{aligned}$$

### 7. Branch statement

$$\begin{aligned} l_0 &: \text{if } (e) \{ \\ l_1 &: \dots \\ l_2 &: \} \text{ else } \{ \\ l_3 &: \dots \\ l_4 &: \} \end{aligned}$$

If  $\text{eval}(e, \sigma_0)$  is true,

$$(l_0, (\sigma_0^E, \sigma_0^K), \sigma_0^H, \sigma_0^D) \rightarrow (l_1, (\sigma_1^E, \sigma_0^K), \sigma_0^H, \sigma_1^D) \text{ where}$$

$$\sigma_1^D = \sigma_0^D + 1$$

and  $(l_2, (\sigma_2^E, \sigma_2^K), \sigma_2^H, \sigma_2^D) \rightarrow (l_4, (\sigma_4^E, \sigma_2^K), \sigma_2^H, \sigma_4^D) \text{ where}$

$$\sigma_4^D = \sigma_2^D + 1$$

Otherwise,  $(l_0, (\sigma_0^E, \sigma_0^K), \sigma_0^H, \sigma_0^D) \rightarrow (l_3, (\sigma_3^E, \sigma_0^K), \sigma_0^H, \sigma_3^D)$  where

$$\sigma_3^D = \sigma_0^D + 1$$

### 8. Return statement

$$\begin{aligned} l_0 &: \text{return } e; \\ l_1 &: \dots \end{aligned}$$

$$(l_0, (\sigma_0^E, \sigma_0^K), \sigma_0^H, \sigma_0^D) \rightarrow (l_1, (\sigma_1^E, \sigma_0^K), \sigma_0^H, \sigma_1^D) \text{ where}$$

$$\begin{aligned} \text{eval}(e, \sigma_0) &= v \\ \sigma_1^E &= \sigma_0^E[\text{RET} \mapsto v] \\ \sigma_1^D &= \sigma_0^D + 1 \end{aligned}$$